

IN THE SPECIFICATION

Please replace paragraphs 0037 and 0051 with the following rewritten paragraphs:

[0037] Then, under the control of the microprocessor 4, the audio processor 21 performs the digital-to-analog conversion on the supplied audio data and outputs it to the speaker (not shown), or converts the supplied audio data into an optical digital sound output signal to output. RAM 22 is bidirectionally coupled to the audio processor 21.

[0051] Further, even when a TS packet included in the input TS data D2C has the same PID as a TS packet for each program specified by the first and second sync code detecting/interchanging unit 31A and 31B, the third sync code detecting/interchanging unit 31C rewrites the sync code of each TS packet to the identification code for internal processing supplied from the microprocessor and interchanges the identical PIDs (Packet Identification) so that the PIDs can be identified. Thereafter, it sequentially stores the TS packet data D10C to the corresponding third FIFO (First-In First-Out) 33C.